

Notice of References Cited	Application/Control No. 10/724,949		Applicant(s)/Patent Under Reexamination AURACHER ET AL.	
	Examiner Nelson Lam		Art Unit 2825	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-6,446,248	09-2002	Solomon et al.	716/17
*	C	US-6,255,845	07-2001	Wong et al.	326/38
*	D	US-2005/0102646	05-2005	Madurawe, Raminda Udaya	716/016
*	E	US-2005/0093573	05-2005	Madurawe, Raminda Udaya	326/041
*	F	US-2004/0222817	11-2004	Madurawe, Raminda Udaya	326/039
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Introduction to Actel FPGA Architecture, Actel Corporation Application Note, September 1997.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.